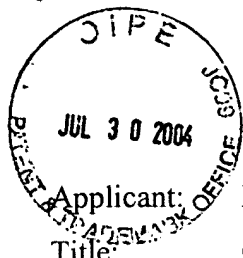


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Ifa

Applicant: Edwin Kan et al.

Title: CHEMORECEPTIVE SEMICONDUCTOR STRUCTURE

Docket No.: 1153.078US1

Serial No.: 10/695,432

Filed: October 28, 2003

Due Date: N/A

Examiner: Unknown

Group Art Unit: 2812

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

☒ A return postcard.

☒ An Information Disclosure Statement (2 pgs.), Form 1449 (4 pgs.), and copies of 39 cited documents.

☒ The International Search Report from International Application No. PCT/US 03/34341 (7 pgs.).

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

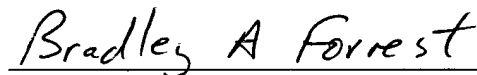
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By: 

Atty: Bradley A. Forrest

Reg. No. 30,837

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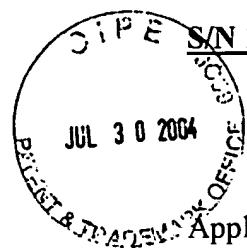
Name



Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

(GENERAL)



S/N 10/695,432

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Edwin Kan et al.	Examiner:	Unknown
Serial No.:	10/695,432	Group Art Unit:	2812
Filed:	October 28, 2003	Docket:	1153.078US1
Title:	CHEMORECEPTIVE SEMICONDUCTOR STRUCTURE		

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Five of the attached documents were discovered as a result of a Search Report in Applicants' corresponding foreign patent application. Enclosed for the Examiner's information is a copy of the Search Report.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

The present application is either a U.S. national patent application filed after June 30, 2003 or an international application that entered the national stage under 35 U.S.C. § 371 after June 30, 2003. Thus, Applicant believes that the U.S. Patent & Trademark Office has waived the requirement under 37 C.F.R. 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication. The waiver is provided in a pre-OG notice from the U.S. Patent & Trademark Office entitled "Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003" and dated July 11, 2003. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

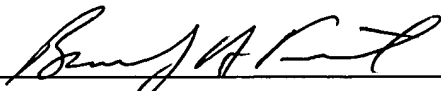
Respectfully submitted,

EDWIN KAN ET AL.

By their Representatives,

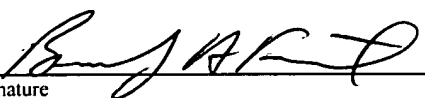
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 373-6972

Date 7-27-2004

By 
Bradley A. Forrest
Reg. No. 30,837

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Bradley A Forrest
Name


Signature

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/695,432
Filing Date	October 28, 2003
First Named Inventor	Kan, Edwin
Group Art Unit	2812
Examiner Name	Unknown

Sheet 1 of 4

Attorney Docket No: 1153.078US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	2002/0117659A1	08/29/2002	Lieber, Charles M., et al.	257	14	12/11/2001
	US-4,232,326	11/04/1980	Neidig, Arno , et al.	257	253	07/26/1978
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	US-4,636,827	01/13/1987	Rudolf, Felix	257	253	09/20/1985
	US-5,071,770	12/10/1991	Kolesar Jr., Edward S.	436	151	11/05/1990

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>	Complete if Known <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 40%;">Application Number</td> <td>10/695,432</td> </tr> <tr> <td>Filing Date</td> <td>October 28, 2003</td> </tr> <tr> <td>First Named Inventor</td> <td>Kan, Edwin</td> </tr> <tr> <td>Group Art Unit</td> <td>2812</td> </tr> <tr> <td>Examiner Name</td> <td>Unknown</td> </tr> </table>	Application Number	10/695,432	Filing Date	October 28, 2003	First Named Inventor	Kan, Edwin	Group Art Unit	2812	Examiner Name	Unknown
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First Named Inventor	Kan, Edwin										
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Examiner Name	Unknown										
Sheet 2 of 4	Attorney Docket No: 1153.078US1										

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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DATE CONSIDERED

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Application Number	10/695,432
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Group Art Unit	2812
Examiner Name	Unknown

Sheet 3 of 4

Attorney Docket No: 1153.078US1

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		MINCH, BRADLEY A., "A Folded Floating-Gate Differential Pair for Low-Voltage Applications", <u>The 2000 IEEE International Symposium on Circuits and Systems, Proceedings, ISCAS 2000 Geneva, Volume: 4, (May 2000), 253-256</u>	
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		STEINER, F. P., et al., "Polymer Coated Capacitive Microintegrated Gas Sensor", <u>8th International Conference on Solid-State Sensors and Actuators and Eurosensors IX. Digest of Technical Papers</u> , (June 1995), 814-817	
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